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FIG. 1 PRIOR ART



FC3 Signal Redistribution

FC2 Voltage V2

FC1 Signal Redistribution

IP1 Ground

IP2 Voltage V1

IP3 Ground

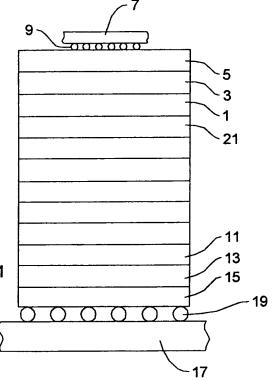
IP4 Voltage V2

BC1 Signal Redist. + Ground

BC2 Voltage V2

BC3 Signal Redist. + Voltage V1

BC4 BGA Pad Layer + Ground





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FIG. 2 PRIOR ART

